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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/787,148	02/27/2004	Choong-Bin Lim	9862-000019/US	4630
30593	7590	02/05/2008	EXAMINER	
HARNESS, DICKEY & PIERCE, P.L.C.			SUN, SCOTT C	
P.O. BOX 8910			ART UNIT	PAPER NUMBER
RESTON, VA 20195			2182	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)
	10/787,148	LIM ET AL.
	Examiner	Art Unit
	Scott Sun	2182

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 25 October 2007.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-18 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-5,7-9 and 11-18 is/are rejected.
- 7) Claim(s) 6 and 10 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____	5) <input type="checkbox"/> Notice of Informal Patent Application
	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed 10/25/2007 have been fully considered but they are not persuasive. Applicant's arguments are summarized as:
 - a. Prior art of record does not teach "adjusting respective buffer capacities".
2. Regarding argument 'a', examiner notes that applicant argues that prior art of record does not teach adjusting buffer overall capacity, e.g. a maximum capacity of the buffer (throughout page 8 of remarks). However, the claim does not distinguish between current and maximum capacities of the buffer. Rather, the claim merely states adjusting "respective buffer capacities of the buffers" with no mention to the type of capacity. Therefore, it is noted that the features upon which applicant relies (i.e. "overall" or "maximum" capacity) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).
3. Having responded to each of applicant's arguments, examiner notes that prior art of record still provide a valid ground of rejection. New rejections are provided for the newly added claims 17 and 18.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 1-5, 7-9, 11-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over applicant's admitted prior art (hereinafter APA) in view of Terry et al (PG Pub #2004/0027997, hereinafter Terry).

1. Regarding claim 1, applicant's admitted prior art discloses a device (shown in prior art figures 3 and 6) for controlling a first plurality of endpoints (endpoints; figure 3) of a USB device, the device comprising: a plurality of buffers ("ping pong" buffers; figure 3, 6) allocated to the first plurality of endpoints, respectively (background; paragraph 11, 23); and an endpoint buffer controller (MCU 626; figure 6) for managing an exchange of packets between a host and the USB device (paragraphs 23-24).

Applicant's admitted prior art does not disclose explicitly obtaining buffer-utilization information or adaptively adjusting the buffers' capacities. However, Terry discloses obtaining buffer-utilization information (status of the buffer, quality indicator) for each of endpoints (data flows to which buffers are assigned, paragraph 25) and adaptively adjusting the respective buffer capacities of the buffers allocated each of the endpoints based upon the buffer utilization information (calculating and setting new capacity allocations, paragraphs 16, 24-26). Teachings of applicant's admitted prior art and Terry are from the same field of data buffering.

Therefore, it would have been obvious at the time of invention to combine teachings of applicant's admitted prior art with teachings of Terry by adding the buffer

adjustment logic into the buffer system of applicant's admitted prior art for the benefit of increasing performance of the transmission system (paragraph 15).

2. Regarding claim 2, applicant's admitted prior art and Terry combined disclose claim 1, and applicant's admitted prior art further discloses wherein each for the plurality of buffers has a plurality of units and a maximum size (maximum packet size) of unit_size x Z, where Z is a positive integer representing the total number of units per buffer, respectively (paragraph 25). Examiner notes that computer memory by definition is organized into a plurality of fixed size units (typically bytes).

3. Regarding claim 3-5, 7, applicant's admitted prior art and Terry combined disclose claim 1, and Terry further discloses counting NAK in a certain time period as a quality indicator to determine quality of channel and corresponding buffer sizes (paragraph 31). Applicant's admitted prior art and Terry does not disclose explicitly the specific hardware, as claimed by applicant, for implementing the method. However, such hardware would have been obvious design choices for a person of ordinary skill in the art in light of the teachings of Terry and applicant's admitted prior art. For example, a timer would be needed to track the time period taught by Terry, a counter would be needed to keep the count of NAK signals taught by Terry.

4. Regarding claim 8, applicant's admitted prior art and Terry combined disclose claim 1 and Terry further discloses wherein the buffers are first-in, first-out (FIFO) buffers (paragraph 12).

5. Regarding claims 9, 12-16, examiner notes that these claims contain limitations that are substantially similar to the above rejected claims, the same grounds of rejection

are applied. Note for claim 13 that applicant's admitted prior art disclose using SIE (serial interface engine; figure 2, paragraph 8) as interface to a USB host.

6. Claims 17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over APA in view of Terry further in view of Georgiou et al (US Patent #7,003,597).
7. Regarding claim 17, APA and Terry combined disclose substantial portions of the claim (see rejection of claim 1) but do not disclose explicitly adjusting a number of the buffers allocated to each of the endpoints based upon the buffer utilization. However, Georgiou discloses adjusting a number of the buffers allocated based upon the buffer utilization (reallocating the more buffers to compensate for overused buffers) column 3, lines 23-27). Teachings of APA, Terry, and Georgiou are from the same field of buffer space allocation.

Therefore, it would have been obvious at the time of invention to combine teachings of applicant's admitted prior art with teachings of Terry and further with teachings of Georgiou by using buffer reallocation scheme of Georgiou in the combined system of Terry and APA for the benefit of decreasing wasted buffer space (column 3, lines 14-16).

8. Regarding claim 18, the limitation is substantially similar to that in claim 1. The same grounds of rejection is applied.

Allowable Subject Matter

6. Claims 6 and 10 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. See previous office action dated 9/29/2006 for reasons for allowance.

Conclusion

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Scott Sun whose telephone number is (571) 272-2675. The examiner can normally be reached on Mon-Thu, 10:00am-8pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Henry Tsai can be reached on (571) 272-4176. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

SS


HENRY TSAI
SUPERVISORY PATENT EXAMINER